

Fig. 1-1 Transfer of information with registers

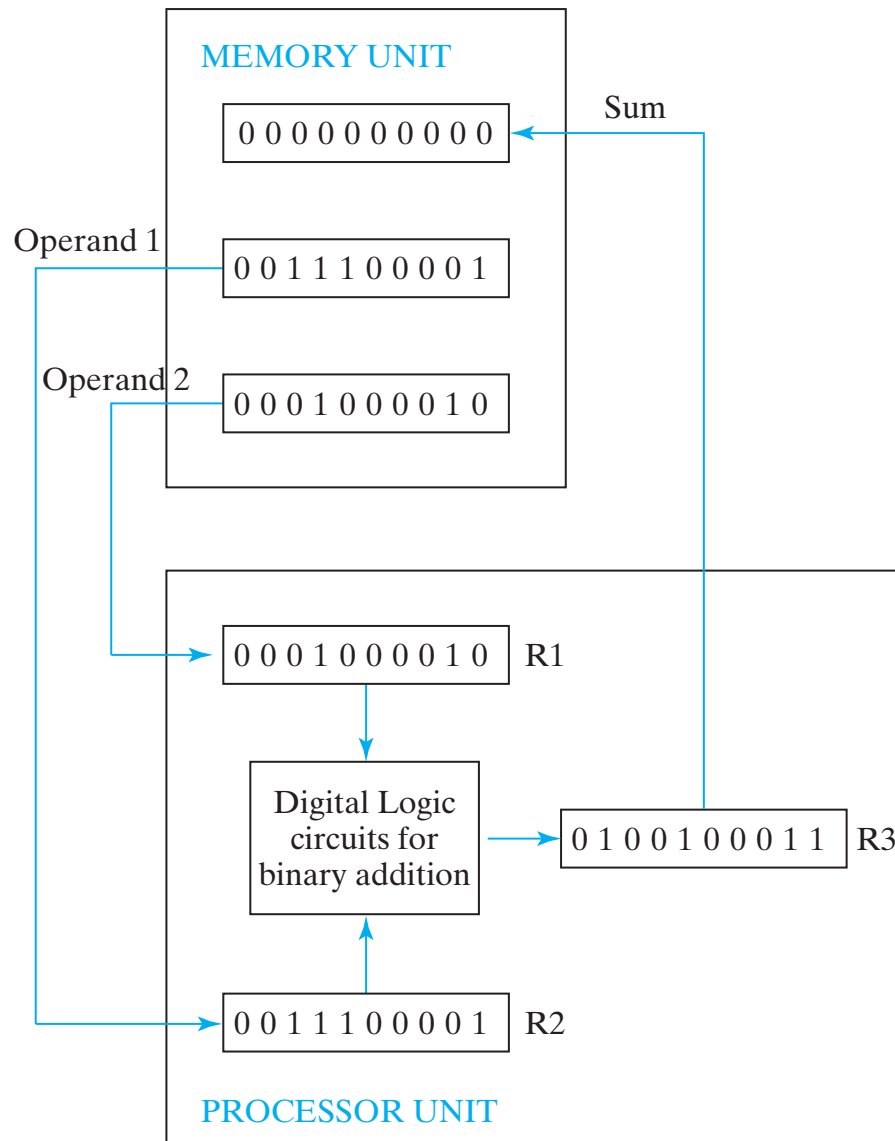


Fig. 1-2 Example of binary information processing

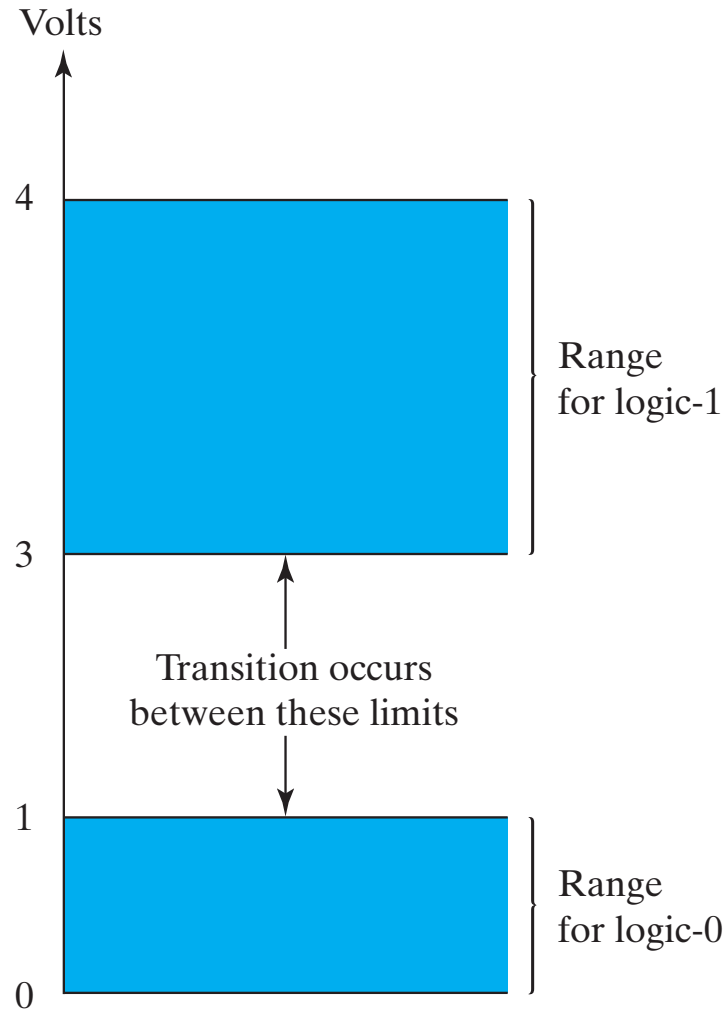
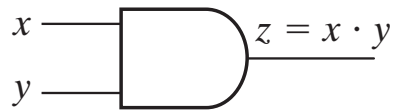
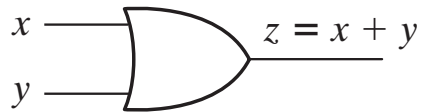


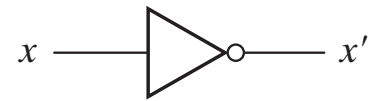
Fig. 1-3 Example of binary signals



(a) Two-input AND gate



(b) Two-input OR gate



(c) NOT gate or inverter

Fig. 1-4 Symbols for digital logic circuits

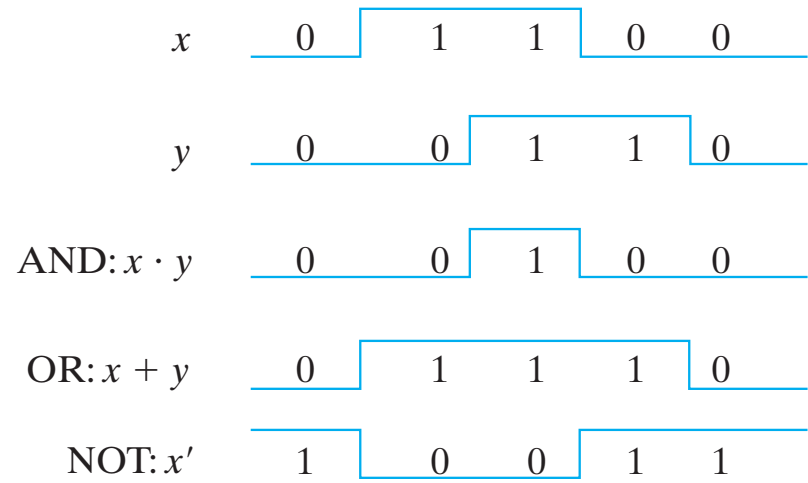
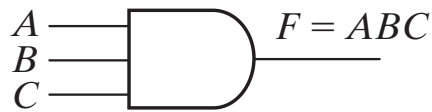
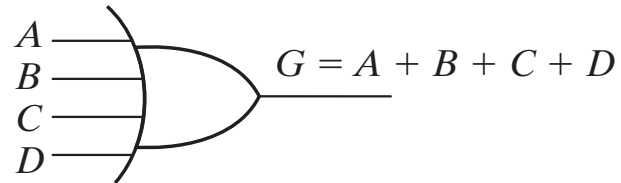


Fig. 1-5 Input-output signals for gates



(a) Three-input AND gate



(b) Four-input OR gate

Fig. 1-6 Gates with multiple inputs