NODAL AND LOOP ANALYSIS TECHNIQUES

LEARNING GOALS

NODAL ANALYSIS LOOP ANALYSIS

Develop systematic techniques to determine all the voltages and currents in a circuit

CIRCUITS WITH OPERATIONAL AMPLIFIERS

Op-amps are very important devices, widely available, that permit the design of very useful circuit...

and they can be modeled by circuits with dependent sources





NODE ANALYSIS

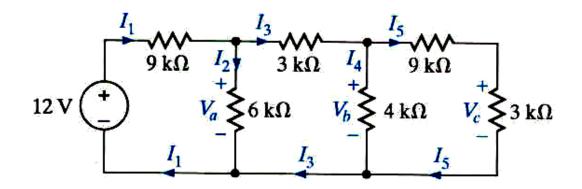
• One of the systematic ways to determine every voltage and current in a circuit

The variables used to describe the circuit will be "Node Voltages" -- The voltages of each node with respect to a pre-selected reference node





IT IS INSTRUCTIVE TO START THE PRESENTATION WITH A RECAP OF A PROBLEM SOLVED BEFORE USING SERIES/ PARALLEL RESISTOR COMBINATIONS

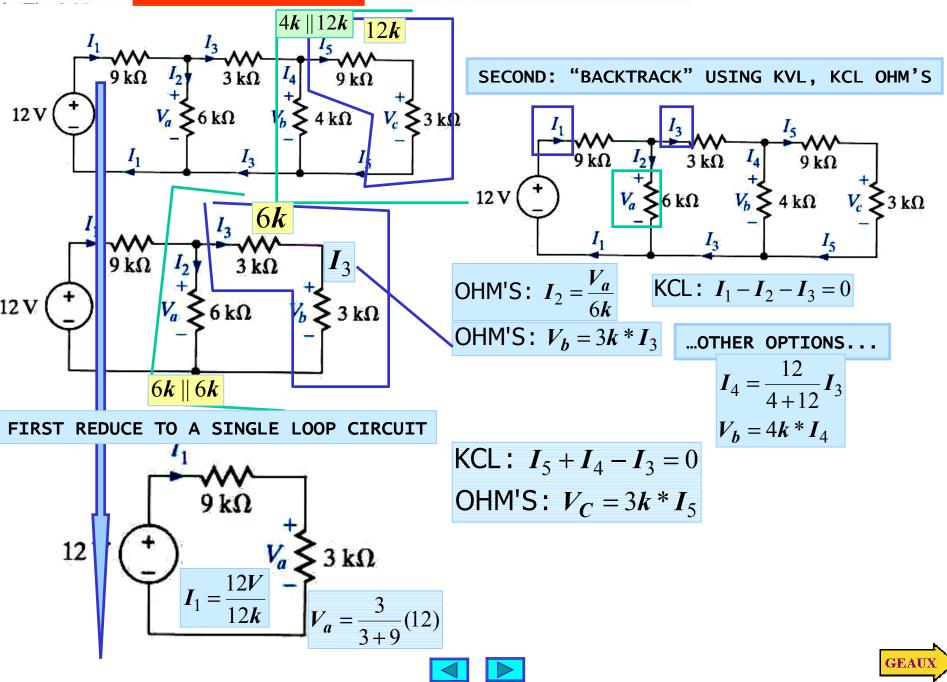


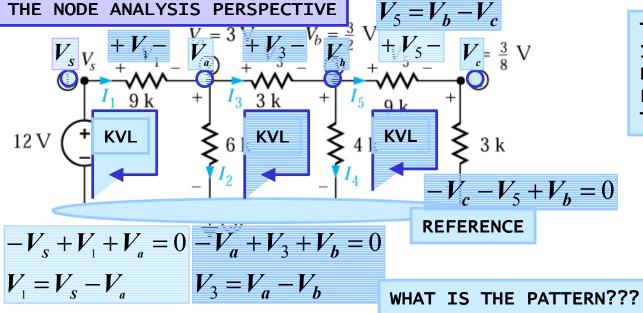
COMPUTE ALL THE VOLTAGES AND CURRENTS IN THIS CIRCUIT





We wish to find all the currents and voltages labeled in the ladder network shown





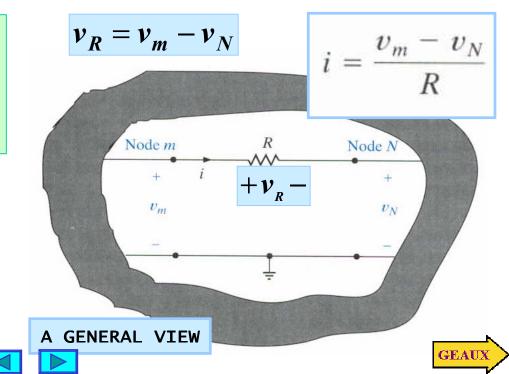
THERE ARE <u>FIVE NODES.</u> IF ONE NODE IS SELECTED AS REFERENCE THEN THERE ARE FOUR VOLTAGES WITH RESPECT TO THEREFERENCE NODE

> ONCE THE VOLTAGES ARE KNOWN THE CURRENTS CAN BE COMPUTED USING OHM'S LAW

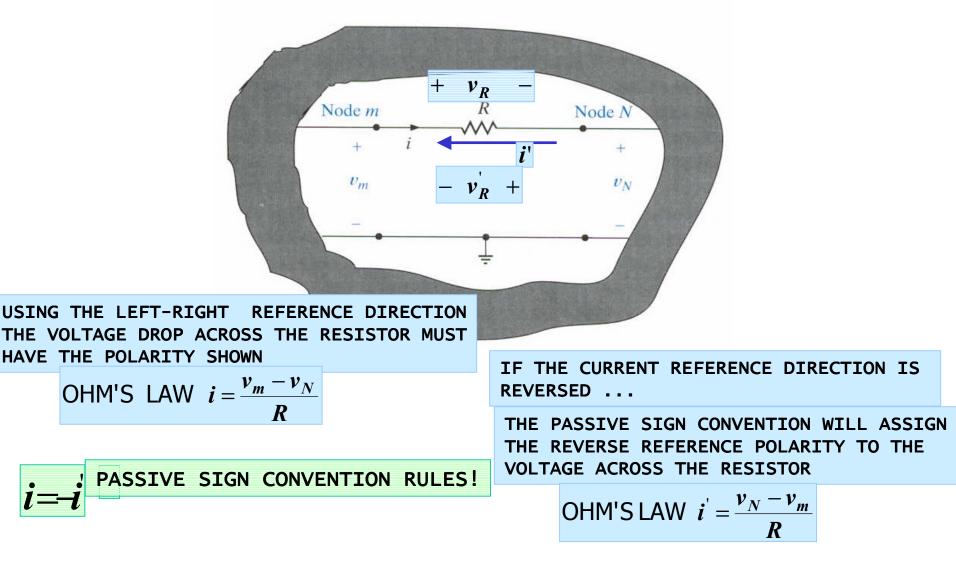
THEOREM: IF ALL NODE VOLTAGES WITH RESPECT TO A COMMON REFERENCE NODE ARE KNOWN THEN ONE CAN DETERMINE ANY OTHER ELECTRICAL VARIABLE FOR THE CIRCUIT

DRILL QUESTION

ca



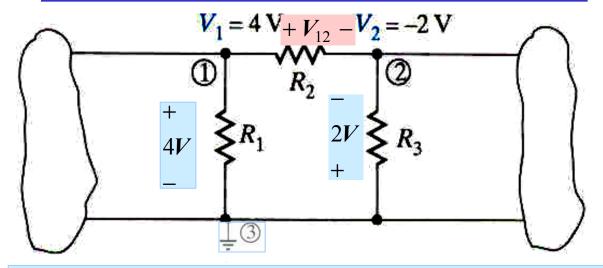
THE REFERENCE DIRECTION FOR CURRENTS IS IRRELEVANT







DEFINING THE REFERENCE NODE IS VITAL



THESTATEMENT $V_1 = 4V$ IS MEANINGLESS

UNTIL THE REFERENCE POINT IS DEFINED

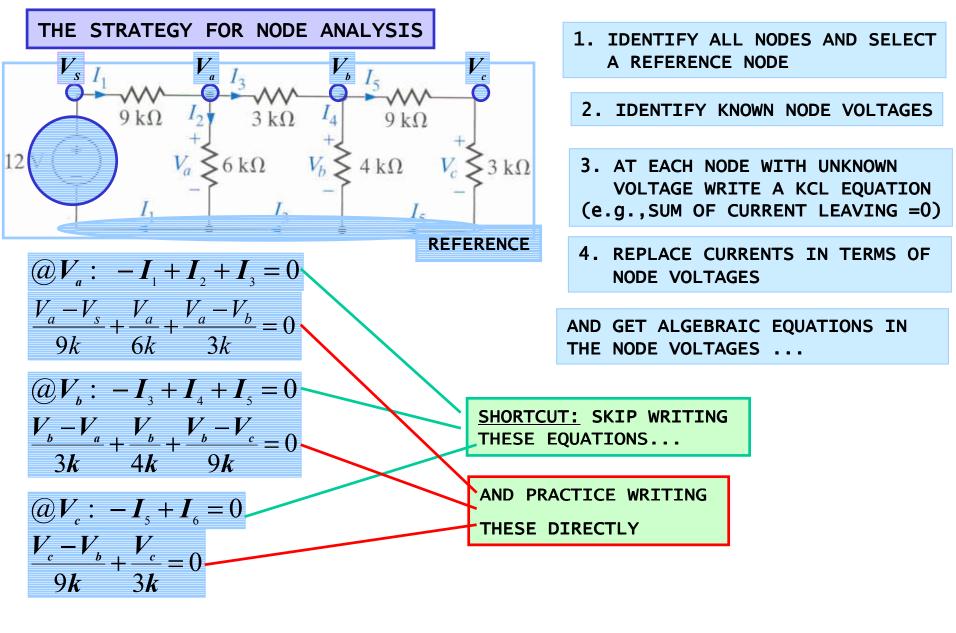
BY CONVENTION THE GROUND SYMBOL SPECIFIES THE REFERENCE POINT.

ALL NODE VOLTAGES ARE MEASURED WITH RESPECT TO THAT REFERENCE POINT

$$V_{12} = __?$$



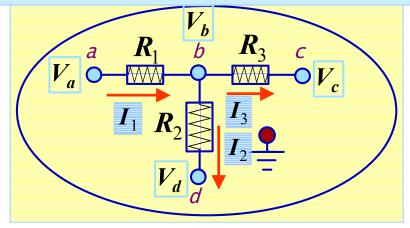






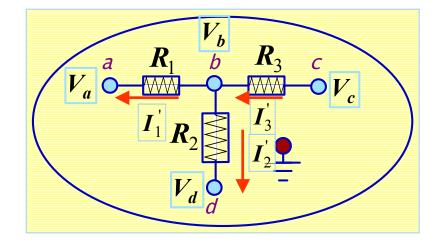


WHEN WRITING A NODE EQUATION... AT EACH NODE ONE CAN CHOSE ARBITRARY DIRECTIONS FOR THE CURRENTS



AND SELECT ANY FORM OF KCL. WHEN THE CURRENTS ARE REPLACED IN TERMS OF THE NODE VOLTAGES THE NODE EQUATIONS THAT RESULT ARE THE SAME OR EQUIVALENT

 $\sum \text{ CURRENTS LEAVING} = 0$ - $I_1 + I_2 + I_3 = 0 \Rightarrow -\frac{V_a - V_b}{R_1} + \frac{V_b - V_d}{R_2} + \frac{V_b - V_c}{R_3} = 0$ $\sum \text{ CURRENTS INTO NODE} = 0$ $I_1 - I_2 - I_3 = 0 \Rightarrow \frac{V_a - V_b}{R_1} - \frac{V_b - V_d}{R_2} - \frac{V_b - V_c}{R_3} = 0$



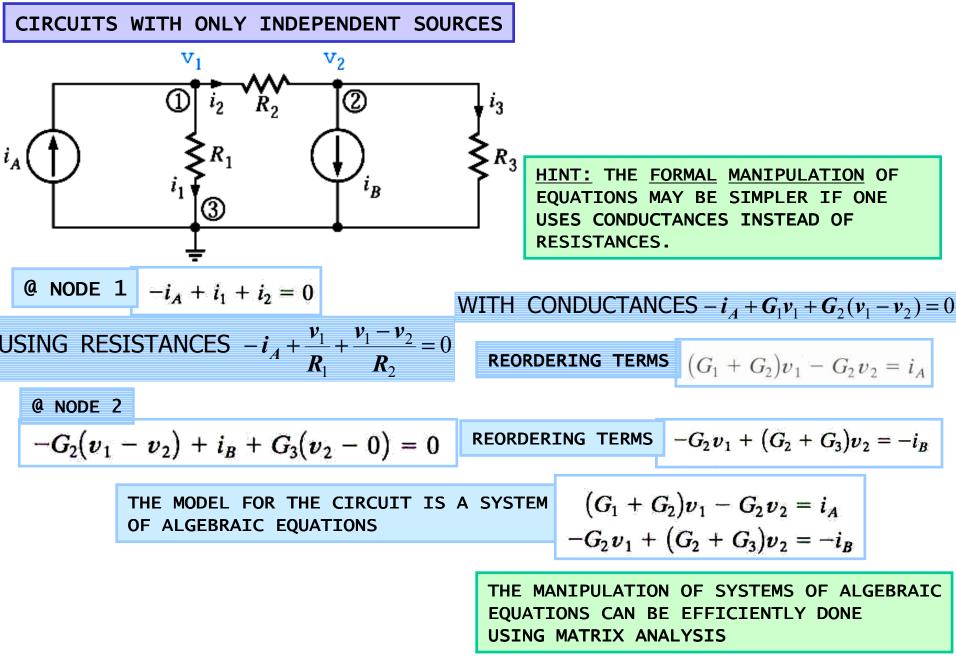
$$\sum \text{ CURRENTS LEAVING} = 0$$
$$I'_1 + I'_2 - I'_3 = 0 \Longrightarrow \frac{V_b - V_a}{R_1} + \frac{V_b - V_d}{R_2} - \frac{V_c - V_b}{R_3} = 0$$

$$\sum \text{ CURRENTS INTO NODE} = 0$$

- $I_1' - I_2' + I_3' = 0 \Rightarrow -\frac{V_b - V_a}{R_1} - \frac{V_b - V_d}{R_2} + \frac{V_c - V_b}{R_3} = 0$

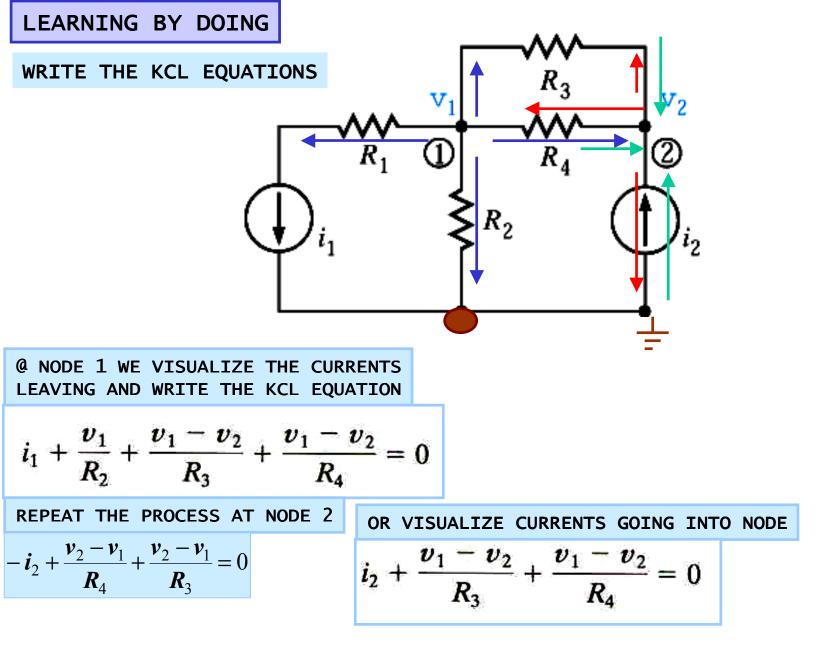
WHEN WRITING THE NODE EQUATIONS WRITE THE EQUATION DIRECTLY IN TERMS OF THE NODE VOLTAGES. BY DEFAULT USE KCL IN THE FORM SUM-OF-CURRENTS-LEAVING = 0

THE REFERENCE DIRECTION FOR THE CURRENTS DOES NOT AFFECT THE NODE EQUATION



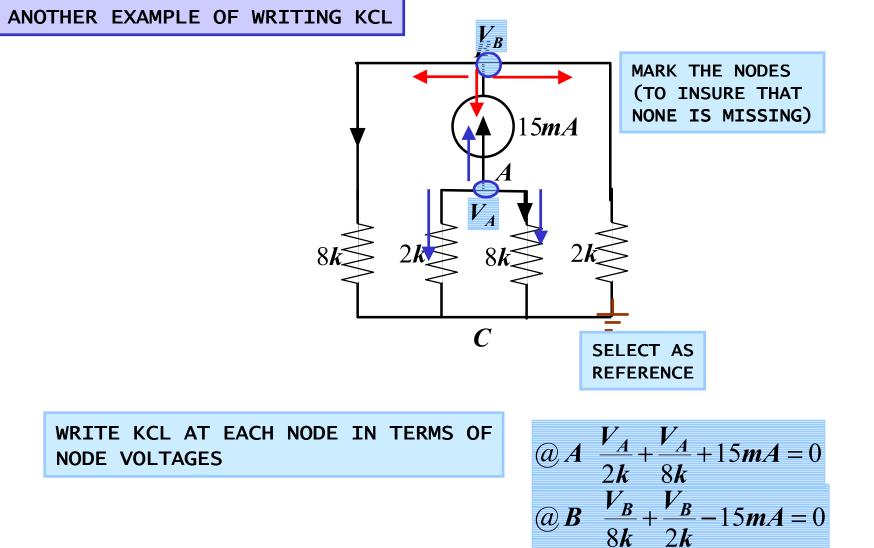






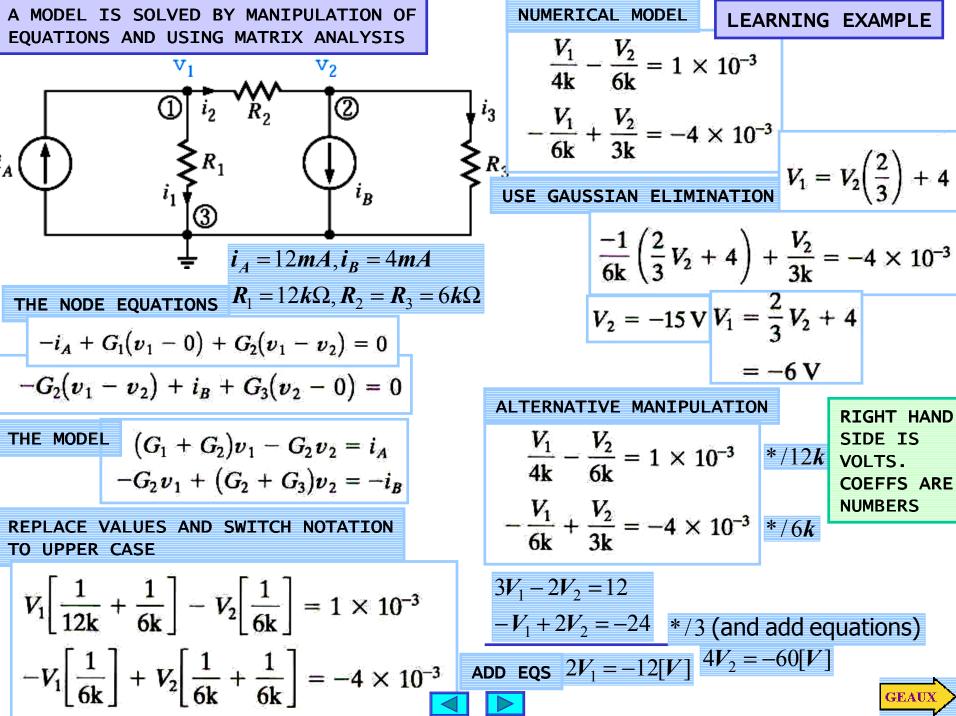


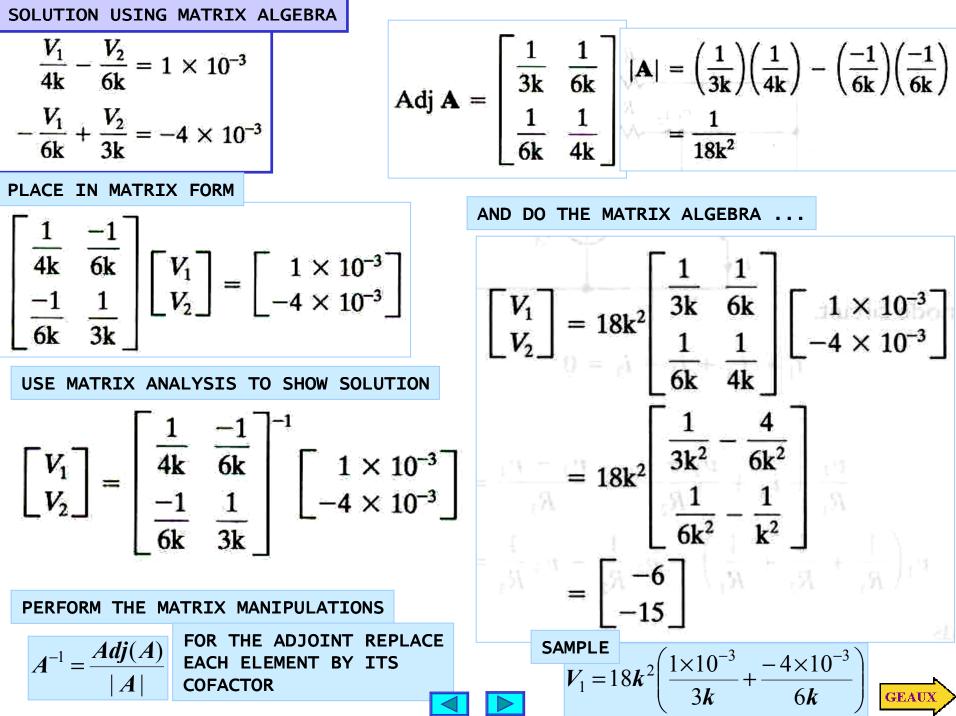


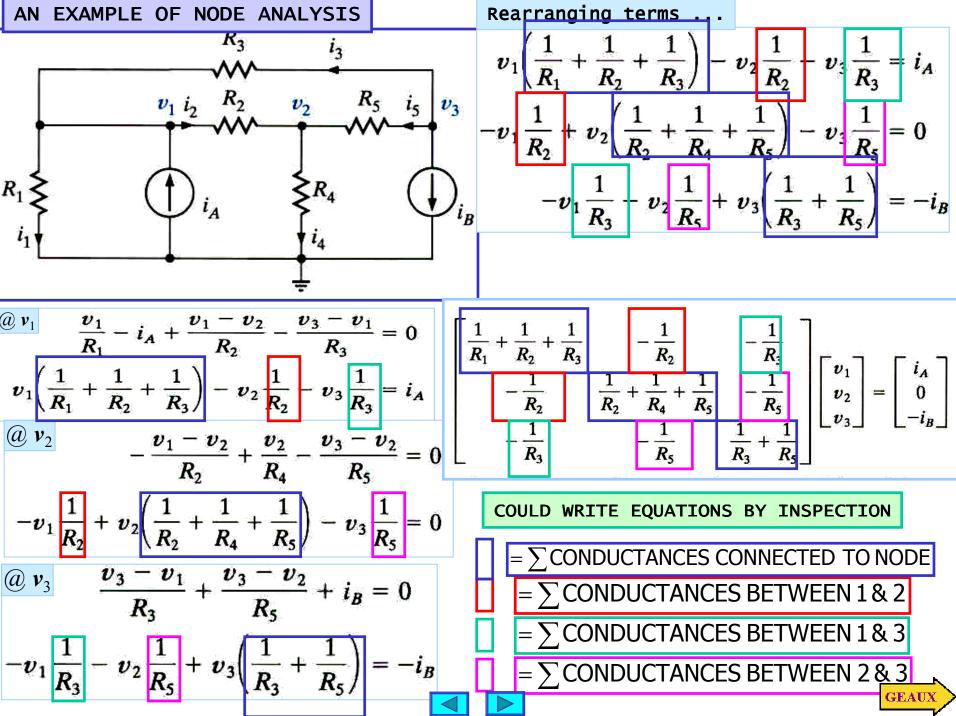


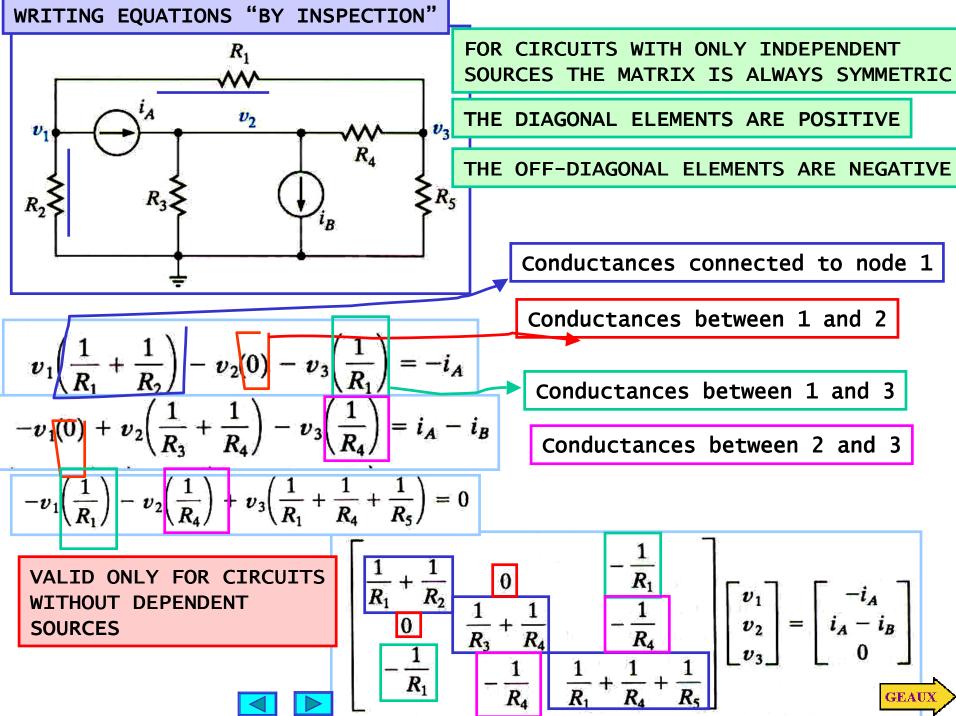






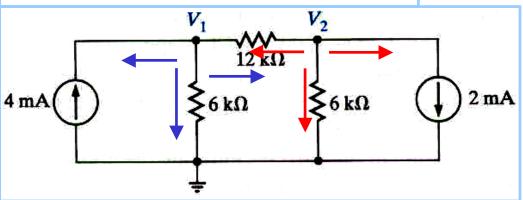






LEARNING EXTENSION

Write the node equations



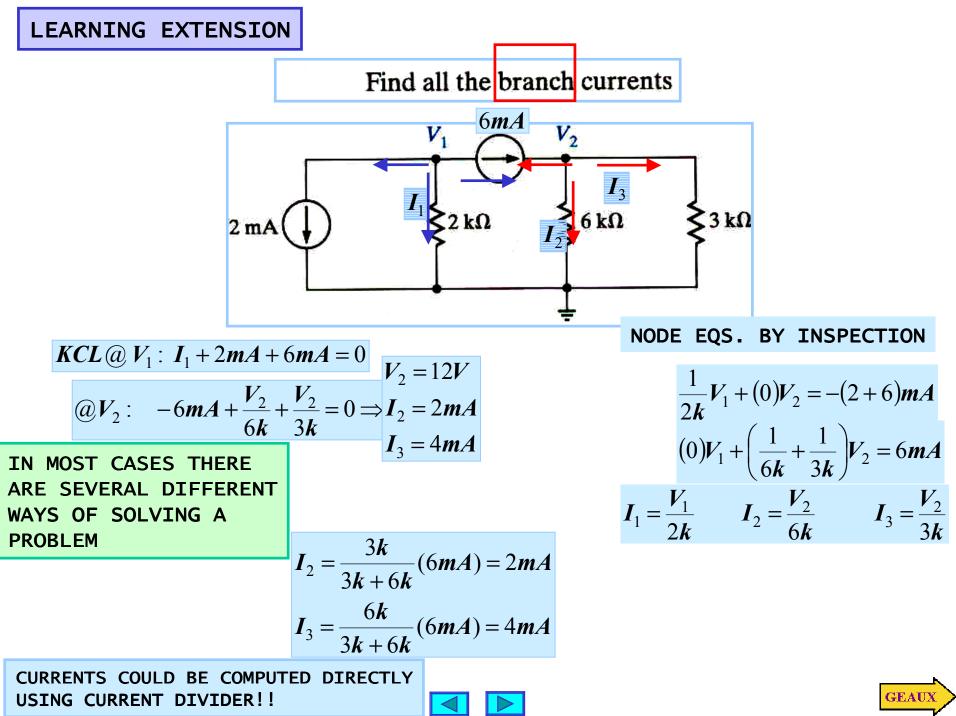
$$(a) V_1: -4mA + \frac{V_1}{6k} + \frac{V_1 - V_2}{12k}$$
 USING KCM
$$(a) V_2: 2mA + \frac{V_2}{6k} + \frac{V_2 - V_1}{12k} = 0$$

BY "INSPECTION"

$$\left(\frac{1}{6k} + \frac{1}{12k}\right)V_1 - \frac{1}{12k}V_2 = 4mA$$
$$-\frac{1}{12k} + \left(\frac{1}{6k} + \frac{1}{12k}\right)V_2 = -2mA$$





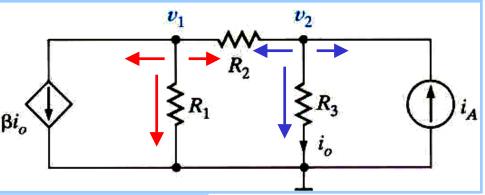


CIRCUITS WITH DEPENDENT SOURCES

CIRCUITS WITH DEPENDENT SOURCES CANNOT BE MODELED BY INSPECTION. THE SYMMETRY IS LOST.

A PROCEDURE FOR MODELING

- •WRITE THE NODE EQUATIONS USING DEPENDENT SOURCES AS REGULAR SOURCES.
- •FOR EACH DEPENDENT SOURCE WE ADD ONE EQUATION EXPRESSING THE CONTROLLING VARIABLE IN TERMS OF THE NODE VOLTAGES



$$\beta i_{o} + \frac{v_{1}}{R_{1}} + \frac{v_{1} - v_{2}}{R_{2}} = 0$$
$$- i_{A} + \frac{v_{2}}{R_{3}} + \frac{v_{2} - v_{1}}{R_{2}} = 0$$

MODEL FOR CONTROLLING VARIABLE

REPLACE AND REARRANGE

$$\left(\frac{1}{R_1} + \frac{1}{R_2}\right)v_1 + \left(\frac{\beta}{R_3} - \frac{1}{R_2}\right)v_2 = 0$$

$$= -\frac{1}{R_2}v_1 + \left(\frac{1}{R_2} + \frac{1}{R_3}\right)v_2 = i_A$$

NUMERICAL EXAMPLE

LEARNING EXAMPLE

$$\beta = 2 \qquad R_2 = 6 \text{ k}\Omega \qquad i_A = 2 \text{ mA}$$
$$R_1 = 12 \text{ k}\Omega \qquad R_3 = 3 \text{ k}\Omega$$

$$\left(\frac{1}{12k} + \frac{1}{6k}\right)v_1 + \left(\frac{2}{3k} - \frac{1}{6k}\right)v_2 = 0$$
$$-\frac{1}{6k}v_1 + \left(\frac{1}{12k} + \frac{1}{3k}\right)v_2 = 2mA$$

$$\frac{1}{4k}V_1 + \frac{1}{2k}V_2 = 0 \qquad */4k$$
$$-\frac{1}{6k}V_1 + \frac{1}{2k}V_2 = 2 \times 10^{-3} */6k$$

$$V_{1} + 2V_{2} = 0$$

-V_{1} + 3V_{2} = 12[V]
ADDING THE EQUATIONS 5V_{2} = 12[V]
$$V_{1} = -\frac{24}{5}V$$





LEARNING EXAMPLE: CIRCUIT WITH VOLTAGE-CONTROLLED CURRENT

REPLACE AND REARRANGE

$$(G_{1} + G_{3})v_{1} - G_{1}v_{2} = i_{A}$$

$$(G_{1} + G_{3})v_{1} - G_{1}v_{2} = i_{A}$$

$$(G_{1} + G_{3})v_{2} - (\alpha + G_{2})v_{3} = -i_{A}$$

$$-G_{2}v_{2} + (G_{2} + G_{4})v_{3} = i_{B}$$
CONTINUE WITH GAUSSIAN ELIMINATION...
WRITE NODE EQUATIONS. TREAT DEPENDENT
SOURCE AS REGULAR SOURCE

$$G_{3}v_{1} + G_{1}(v_{1} - v_{2}) - i_{A} = 0$$

$$i_{A} + G_{1}(v_{2} - v_{1}) + \alpha v_{x} + G_{2}(v_{2} - v_{3}) = 0$$

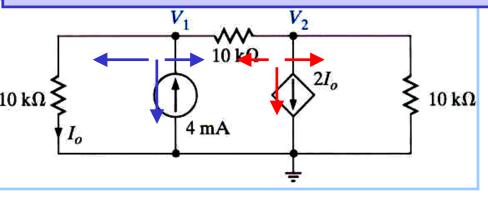
$$G_{2}(v_{3} - v_{2}) + G_{4}v_{3} - i_{B} = 0$$

$$\begin{bmatrix} (G_{1} + G_{3}) & -G_{1} & 0 \\ -G_{1} & (G_{1} + \alpha + G_{2}) & -(\alpha + G_{2}) \\ 0 & -G_{2} & (G_{2} + G_{4}) \end{bmatrix} \begin{bmatrix} v_{1} \\ v_{2} \\ v_{3} \end{bmatrix} = \begin{bmatrix} i_{A} \\ -i_{A} \\ i_{B} \end{bmatrix}$$
EXPRESS CONTROLLING VARIABLE IN TERMS OF
NODE VOLTAGES

$$v_{x} = v_{2} - v_{3}$$
FOUR EQUATIONS IN OUR UNKNOWNS. SOLVE
USING FAVORITE TECHNIQUE

USING MATLAB TO SOL	VE THE NODE EQUATIONS	
$\begin{bmatrix} (G_1 + G_3) & -G_1 \\ -G_1 & (G_1 + \alpha + G_2) \\ 0 & -G_2 \end{bmatrix}$	$ \begin{array}{c} 0\\ 0\\ -(\alpha + G_2)\\ (G_2 + G_4) \end{array} \begin{bmatrix} v_1\\ v_2\\ v_3 \end{bmatrix} = \begin{bmatrix} i_A\\ -i_A\\ i_B \end{bmatrix} \qquad \begin{array}{c} R_1 = 1k\Omega, \ R_2 = R_3 = 2k\Omega, \\ R_4 = 4k\Omega, i_A = 2mA, i_B = 4mA, \\ \alpha = 2[A/V] \end{aligned} $	А,
DEFINE THE COMPONENTS C	OF THE CIRCUIT >> R1=1000;R2=2000;R3=2000; R4=4000; %resistances in Ohm >> iA=0.002;iB=0.004; %sources in Amps >> alpha=2; %gain of dependent source	
DEFINE THE MATRIX G	» G=[(1/R1+1/R2), -1/R1, 0; % first row of the matrix	
Entries in a row are separated by commas (or plain spaces). Rows are separated by semi colon	-1/R1, (1/R1+alpha+1/R2), -(alpha+1/R2); %second row 0, -1/R2, (1/R2+1/R4)], %third row. End in comma to have the G = 0.0015 -0.0010 0	e echo
	-0.0010 2.0015 -2.0005 0 -0.0005 0.0008	
DEFINE RIGHT HAND SIDE VECTOR > I=[iA;-iA;iB]; %end in ";" to skip echo		
SOLVE LINEAR EQUATION	× V=G\I % end with carriage return and get the echo V = 11.9940 15.9910	
	15.9940	

LEARNING EXTENSION: FIND NODE VOLTAGES



$$@V_1: \frac{V_1}{10k} - 4mA + \frac{V_1 - V_2}{10k} = 0 \\ @V_2: \frac{V_2 - V_1}{10k} + 2I_0 + \frac{V_2}{10k} = 0$$

CONTROLLING VARIABLE (IN TERMS ON NODE VOLTAGES)

$$\boldsymbol{I_0} = \frac{\boldsymbol{V_1}}{10\boldsymbol{k}}$$

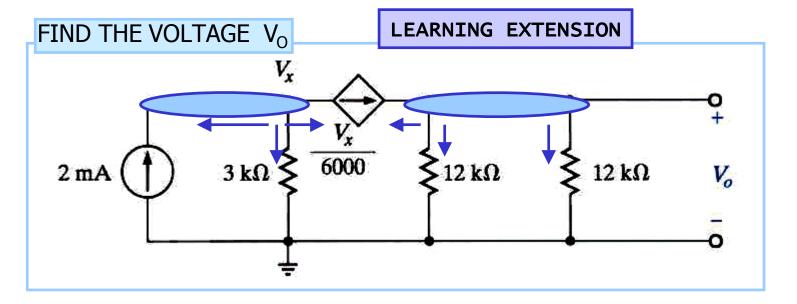
$$\frac{V_1}{10k} - 4mA + \frac{V_1 - V_2}{10k} = 0$$
$$\frac{V_2 - V_1}{10k} + 2\frac{V_1}{10k} + \frac{V_2}{10k} = 0$$

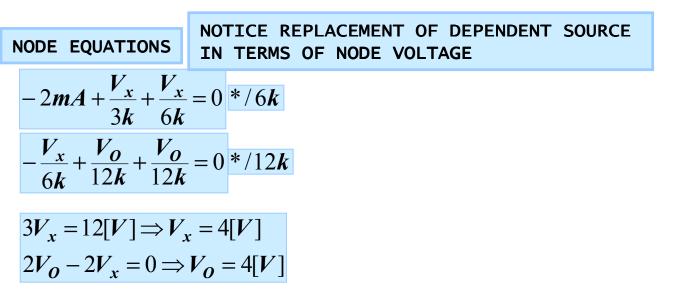
REARRANGE AND MULTIPLY BY 10k $2V_1 - V_2 = 40[V] */2$ and add eqs. $V_1 + 2V_2 = 0$ $5V_1 = 80V \Rightarrow V_1 = 16V$

$$V_2 = -\frac{V_1}{2} \Longrightarrow V_2 = -8V$$





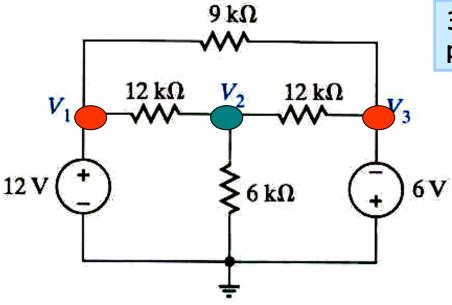








CIRCUITS WITH INDEPENDENT VOLTAGE SOURCES



Hint: Each voltage source connected to the reference node saves one node equation

One more example

3 nodes plus the reference. In principle one needs 3 equations...

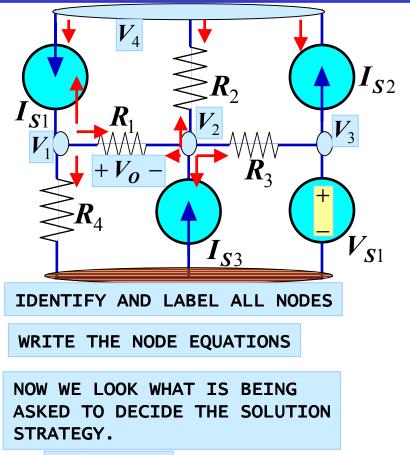
...but two nodes are connected to the reference through voltage sources. Hence those node voltages are known!!!

...Only one KCL is necessary $\frac{V_2}{V_2} + \frac{V_2 - V_3}{V_2 - V_1} + \frac{V_2 - V_1}{V_2 - V_1} = 0$ 6k12**k** $V_1 = 12[V]$ THESE ARE THE REMAINING TWO NODE EQUATIONS $V_3 = -6[V]$ SOLVING THE EQUATIONS $2V_2 + (V_2 - V_3) + (V_2 - V_1) = 0$ $4V_2 = 6[V] \Longrightarrow V_2 = 1.5[V]$





Problem 3.67 (6th Ed) Find V_0



 $V_0 = V_1 - V_2$

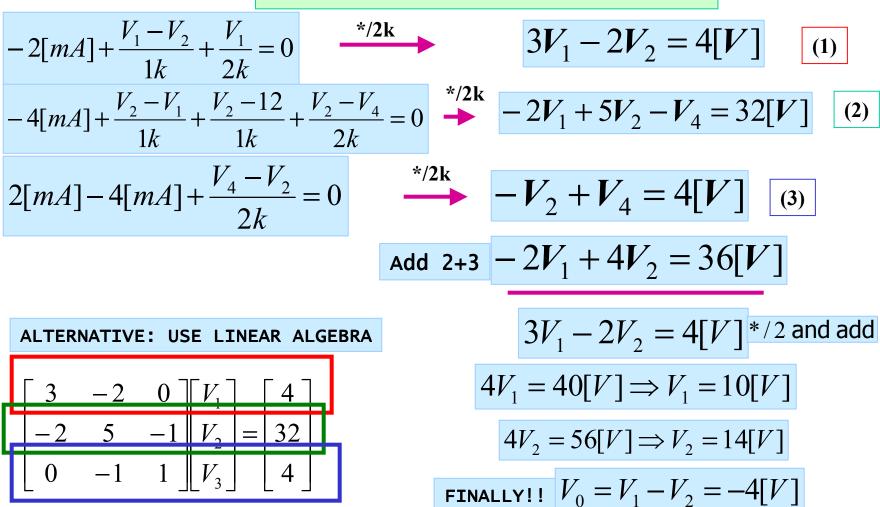
ONLY V_1, V_2 ARE NEEDED FOR V_0

R1 = 1k; R2 = 2k, R3 = 1k, R4 = 2k Is1 =2mA, Is2 = 4mA, Is3 = 4mA, Vs = 12 V





TO SOLVE BY HAND ELIMINATE DENOMINATORS



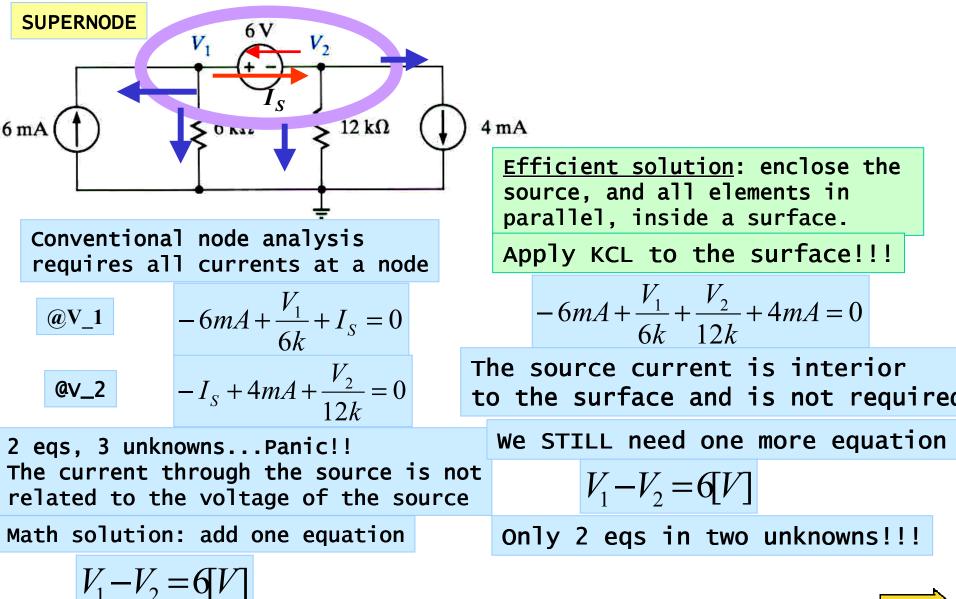
So. What happens when sources are connected between two non reference nodes?





THE SUPERNODE TECHNIQUE

We will use this example to introduce the concept of a SUPERNODE





GEAU.

ALGEBRAIC DETAILS

The Equations

(1)
$$\frac{V_1}{6k} + \frac{V_2}{12k} - 6mA + 4mA = 0 * /12 k$$

(2) $V_1 - V_2 = 6[V]$

Solution

1. Eliminate denominators in Eq(1). Multiply by ...

 $2V_1 + V_2 = 24[V]$ $V_1 - V_2 = 6[V]$

2. Add equations to eliminate V_2 $3V_1 = 30[V] \Rightarrow V_1 = 10[V]$

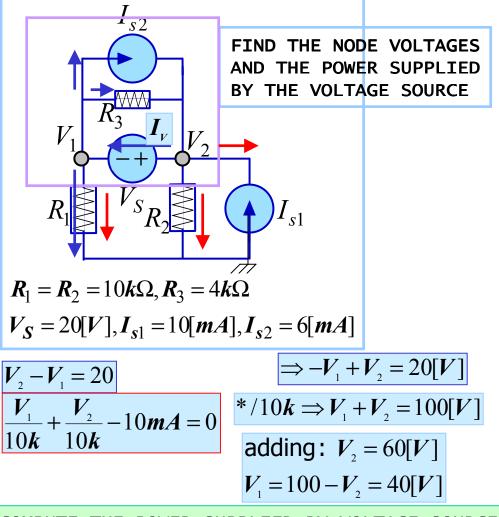
3. Use Eq(2) to compute V_2 $V_2 = V_1 - 6[V] = 4[V]$





The supernode technique

- Used when a branch between two nonreference nodes contains a voltage source.
- First encircle the voltage source and the two connecting nodes to form the supernode.
- Write the equation that defines the voltage relationship between the two nonreference nodes as a result of the presence of the voltage source.
- Write the KCL equation for the supernode.
- If the voltage source is dependent, then the controlling equation for the dependent source is also needed.



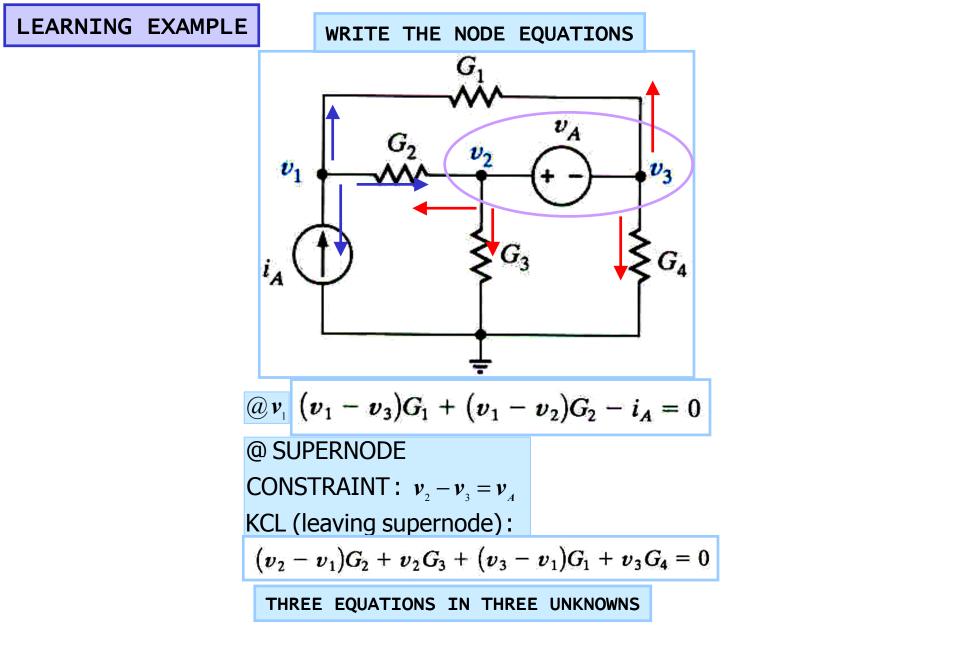
TO COMPUTE THE POWER SUPPLIED BY VOLTAGE SOURCE WE MUST KNOW THE CURRENT THROUGH IT

$$I_{V} = \frac{V_{1}}{10k} + 6mA + \frac{V_{1} - V_{2}}{10k} = \frac{8mA}{P} = 20[V] \times 8[mA] = 160mW$$

BASED ON PASSIVE SIGN CONVENTION THE POWER IS RECEIVED BY THE SOURCE!!

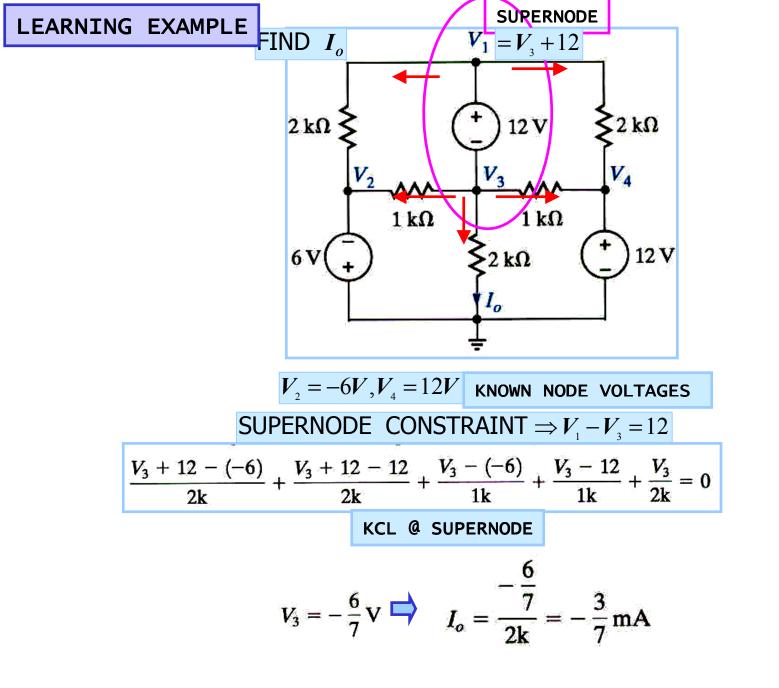






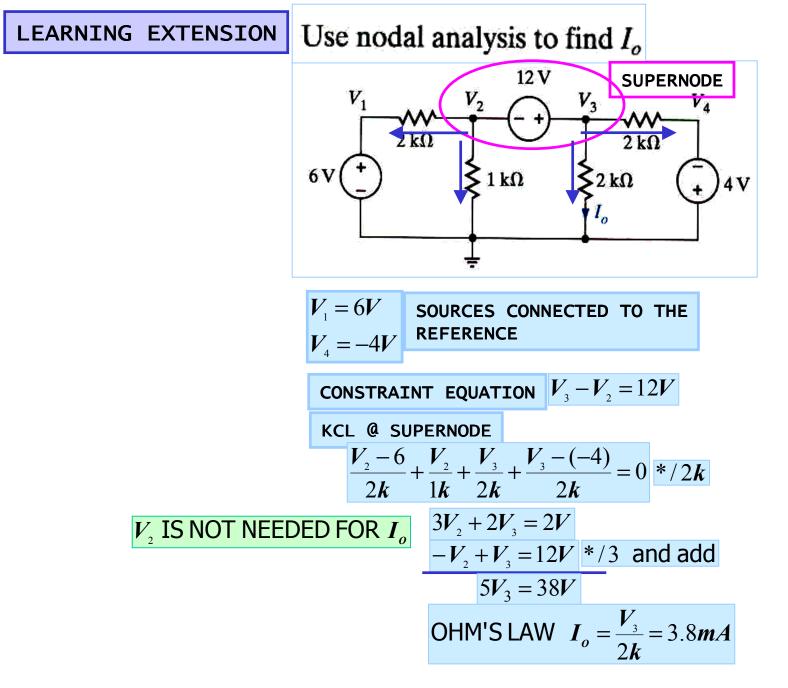
















WRITE THE NODE EQUATIONS

supernode V_2 R_1 R_2 R_5 R_5 R_5 R_7 R_7 R_7 R_7 R_7

- Identify all nodes, select a reference and label nodes
- Nodes connected to reference through a voltage source
- Voltage sources in between nodes and possible supernodes

EQUATION BOOKKEEPING: KCL@ V_3, KCL@ supernode, 2 constraints equations and one known node

Supernodes can be more complex

KCL@V_3
$$\frac{V_3 - V_2}{R_4} + \frac{V_3 - V_4}{R_5} + \frac{V_3}{R_7} = 0$$

KCL @SUPERNODE (Careful not to omit any current)

$$\frac{V_2 - V_1}{R_1} + \frac{V_5 - V_1}{R_2} + \frac{V_5}{R_3} + \frac{V_4}{R_6} + \frac{V_4 - V_3}{R_5} + \frac{V_2 - V_3}{R_4} = 0$$

CONSTRAINTS DUE TO VOLTAGE SOURCES

$$V_1 = V_{S1}$$
$$V_2 - V_5 = V_{S2}$$

$$V_5 - V_4 = V_{S3}$$

5 EQUATIONS IN FIVE UNKNOWNS.





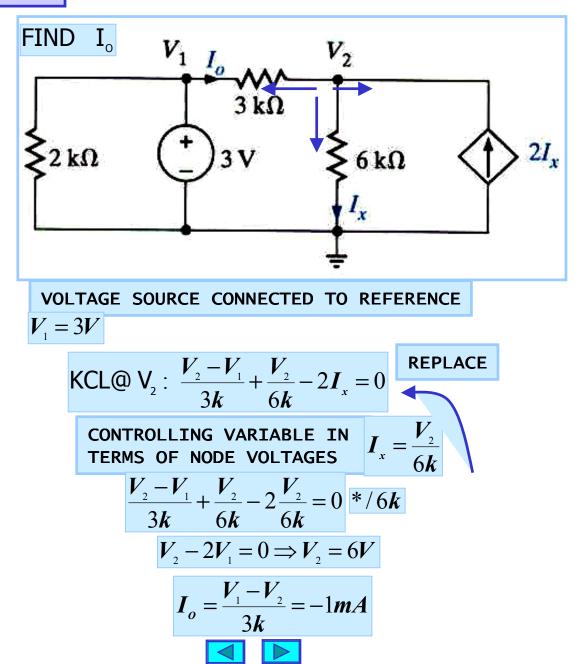
CIRCUITS WITH DEPENDENT SOURCES PRESENT NO SIGNIFICANT ADDITIONAL COMPLEXITY. THE DEPENDENT SOURCES ARE TREATED AS REGULAR SOURCES

WE MUST ADD ONE EQUATION FOR EACH CONTROLLING VARIABLE

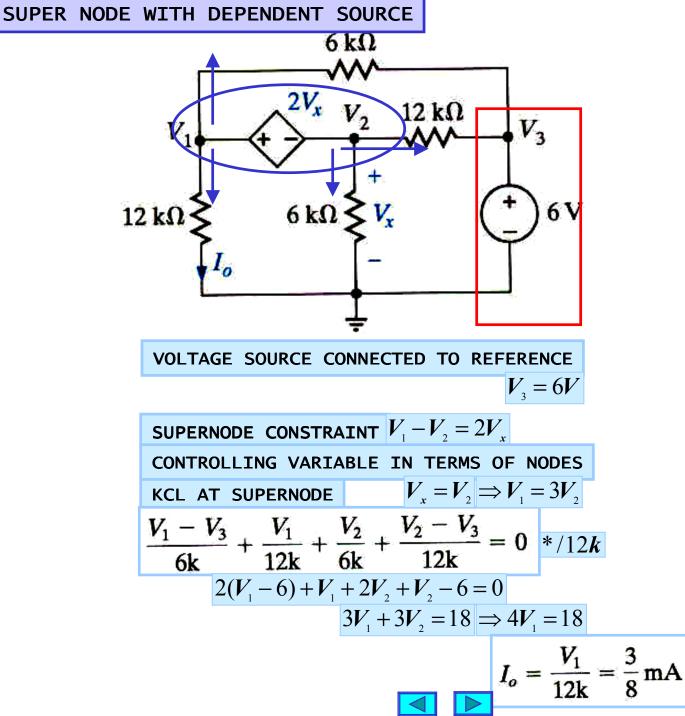




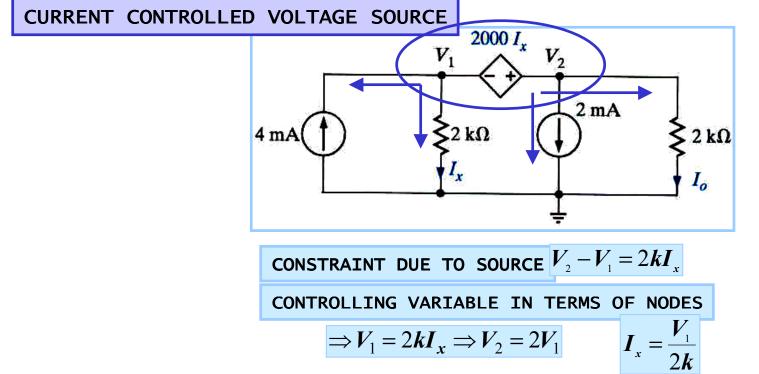
LEARNING EXAMPLE











KCL AT SUPERNODE

$$-4mA + \frac{V_{1}}{2k} + 2mA + \frac{V_{2}}{2k} = 0$$

$$V_{1} + V_{2} = 4[V] * / 2 \text{ and add}$$

$$-2V_{1} + V_{2} = 0$$

$$3V_{2} = 8[V]$$

$$I_{o} = \frac{V_{2}}{2k} = \frac{4}{3}mA$$





An example with dependent sources

